SM-A505FN REV0.4A

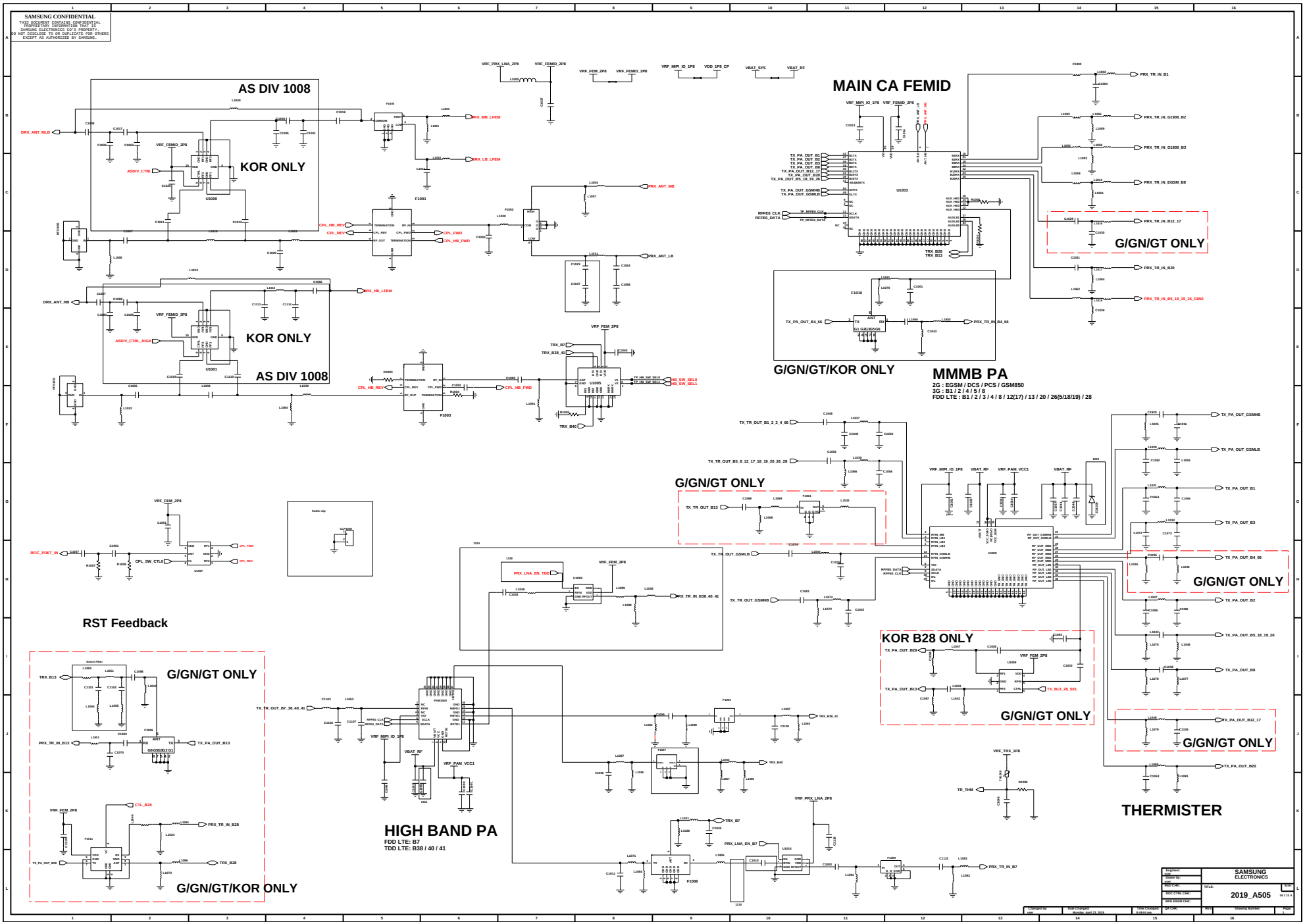
Engineer:
Smart Sy.
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:
QA CHK:

SMD: 12.19

TITLE: SM-A505F

SIZE

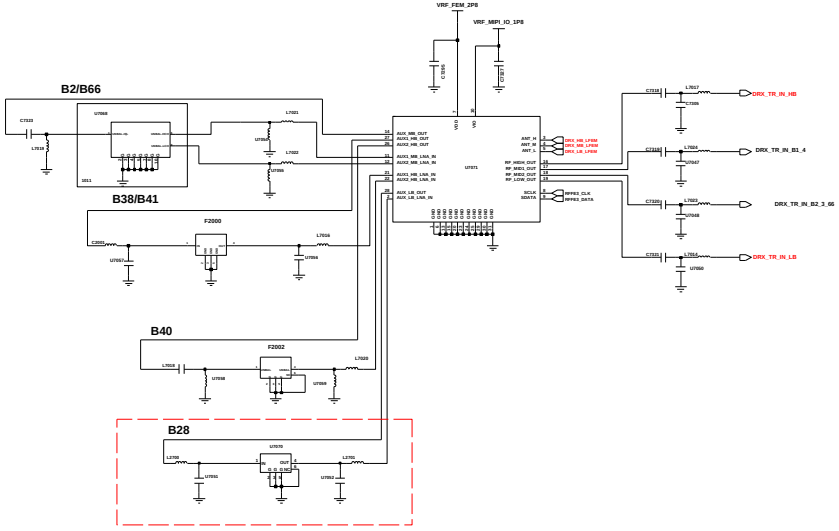
Changed By: (prof)
Date Changed:
Time Changed:
REV
Drawing Number:
Page



Engineer:	SAMSUNG ELECTRONICS	
Drawn by: user		
RDD CHK:	TITLE: 2019_A505	SIZE: 34 X 22 A
DOC CTRL CHK:		
MFG ENGR CHK:		
QA CHK:	REV:	Drawing Number: Page: 1

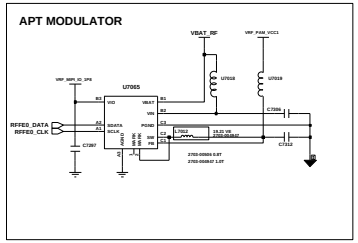
Changed by: user	Date Changed: Monday, April 29, 2019	Time Changed: 4:49:42 pm

DRX



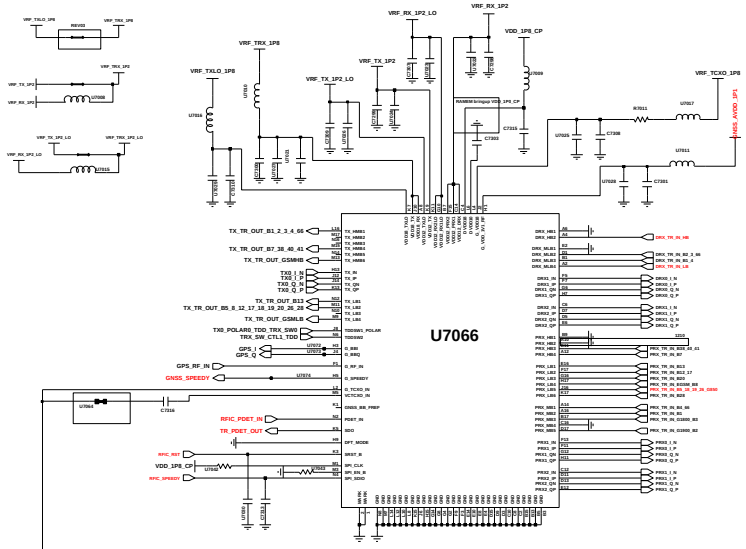
G/GN/GT/KOR ONLY

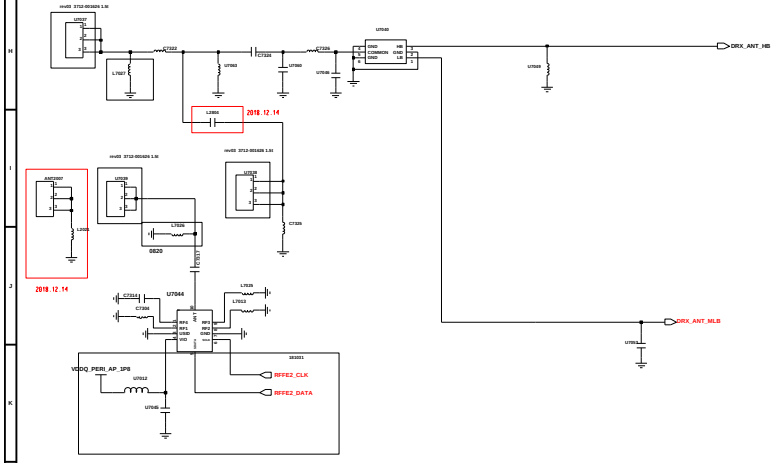
APT MODULATOR



GND

TRANCEIVER





Engineer	DRX, TRANCEIVER
Sheet No.	
R&D CHK:	TITLE:
DOC CTRL CHK:	2019 A505
MFG ENGR CHK:	
QA CHK:	Drawing Number

Changed by:	Date Changed:	Time Changed:

MST

NFC

NFC LDO

NFC	NFC PART	Power supply of SIM1		NFC GPIO 14	
	U3006	R6002	R6000	R4013	R4014
NFC	CONNECT	short	open	10K ohm	open
nonNFC	NC	open	short	open	10K ohm

BT/WIFI FM

GPS

SENSORS

RGB sensor

HALL_IC

DMB
(for KOR)

181114_DELETED

FM/TDMB diplexer

DTV
Option for GT

SENSOR LDO

Proxi Sensor

6 AXIS(ACC+GRYO)

MAGNETIC

Grip Sensor

Engineer:	
Drawn by:	SENSOR, WIFI/BT/GPS/NFC
RED CHK:	
SDC CTRL CHK:	TITLE: 2019 A505
WIFI BT/GPS CHK:	
QA CHK:	

EXYNOS9610(RAMEN) POP MEMORY

LP4x 6G : 1105-003007, K4UJE3T4AM-CGCL, SEC
LP4x 6G : 1105-003011, MT53D1536M32D6ME-046WT:D, MICRON
LP4x 4G : 1105-003008, K4UBE3DAM-CGCL, SEC
LP4x 4G : 1105-003012, MT53D1024M32D4BD-046WT:D, MICRON

UCP400-1
JTAG
BOOT
PMU
CLOCK
OTP
ADC
UART
SPI
I2C
I2S
CAM
CMGP
SHUB
PWM/TE
EINT
GPIO

UCP400-2
CP GPIO
CP UART
ANT-SW LNA CTRL
USIM
RFIC CTRL
BT/WIFI/FM
MIPI RFFE
RX ADC0
RX ADC1
RX ADC2
TX DAC0
TX DAC1
ET DAC
GNSS

UCP400-4
USB 2.0
USB 3.0
MIPI
MSH
UFS

UCP400-3

UCP400-5

I2C Pull-up Register

PCB JTAG

AP JTAG

BOOT option (need to update for RAMEN)

GNSS[1:0]	S14	2nd
000	UFS	USB
001	USB	
010	SD CARD	USB
011	UFS	SD CARD

H/W Revision Setting

REV	HW_VER3	HW_VER2	HW_VER1	HW_VER0
	0	0	0	0
0.0	0	0	0	1
0.1	0	0	1	0
0.2	0	0	1	1
0.3	0	1	0	0
0.4C	0	1	0	1
0.4/0A	0	1	1	0

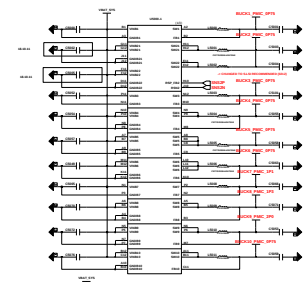
MST	NFC	DTV
MST	NFC	DTV
NONMST	NONNFC	NONDTV

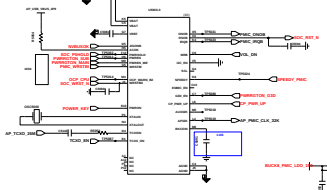
DRAM(POP)	EMMC
3G	32G
4G	64G
6G	128G

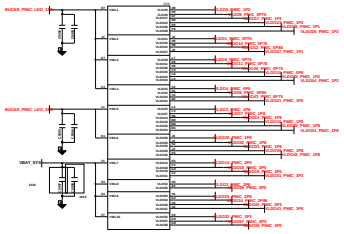
Project	AP
Engineer	
Date	2019 A505
Rev	
Schematic Ver	
PCB Ver	

user|swcad100|10.253.20.206|20190213-153559

MAIN_PMIC

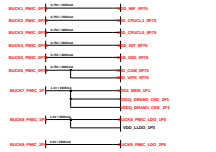




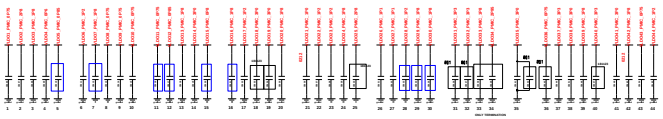


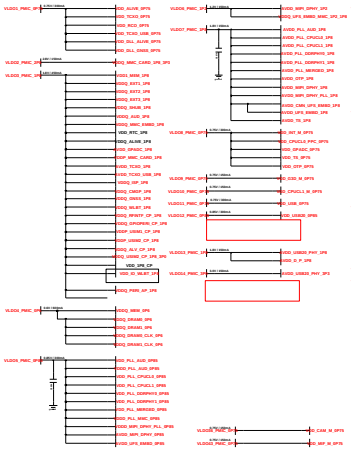
SYSTEN POWER

BUCK POWER

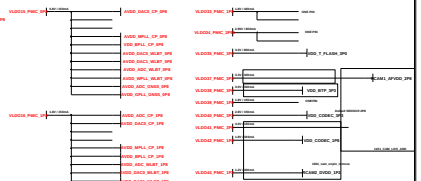


SYSTEM POWER



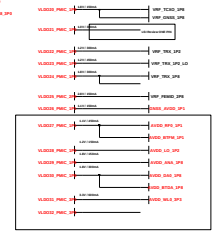


CP POWER



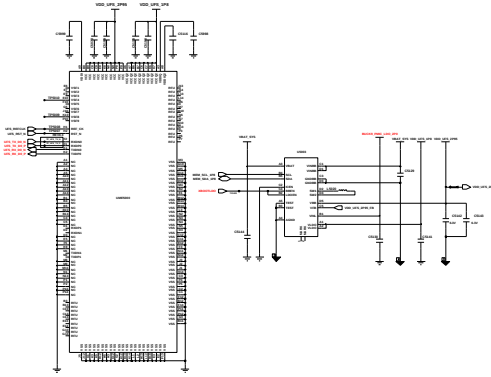
GENERAL PERI POWER

RF & S620 POWER

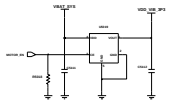


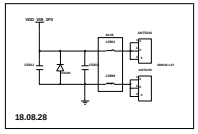
UFS MEMORY

64GB : 1107-002536, KLUCG4J1ED-B0C1, SEC (0271)
64GB : 1107-002588, THGAF8G9T43BAIR, THOSIBA
128GB : 1107-002573, KLUDG4U1EA-B0C1, SEC
128GB : 1107-002611, THGAF8T0T43BAIR, THOSIBA



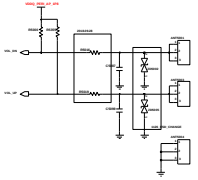
IF_PMIC SM5713



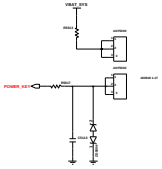


18.08.28

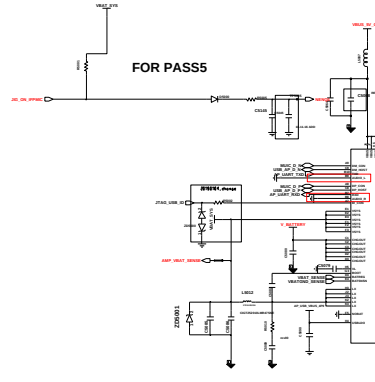
MOTOR



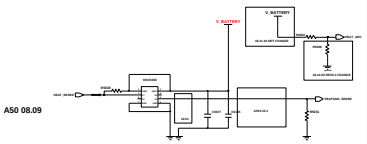
VOLUME KEY



POWER KEY



FOR PASS5



BATTERY CON

A50 08.09

CHG THEM

Engineer		PMIC/IF PMIC/eMMC	
Schematic			
Layout Designer		TITLE	
Date Created		2019 A505	
Date Modified			
Designer/PIC	Team Designer	Date Designed	Size / Rev	Drawing Number	Sheet no.

AUDIO

Audio CODEC

SPK AMP

Ear MIC

MAIN MIC

NEAR TO CODEC GND_ISOLATION

NEAR TO CODEC GND_ISOLATION

RCV CONTACT

MAIN FPCB

Sub MIC

SIM & SD CARD (3IN3)

SHIELD CAN

BOTH HOLE

08.10 OVP CIRCUIT CHANGE(FOR FAST CHARGING)

DTV DETECT OPEN

<DTV>

F/F/N/G: NC
MT : ONLY

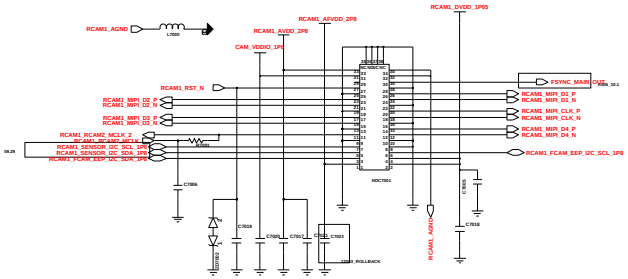
2D BARCODE

	FN (F&N)	FM (CS)	F (SIM1/MS1)	G (LA)	GN (SIM1/LA/TS)	GT (LA, Knock)	L (EA)	NA (UA)
F R6032	Solder	Solder	Solder			Solder		Solder
G R6033				Solder	Solder	Solder		Solder
N R6006	Solder	Solder			Solder	Solder		Solder
K R6018		Solder					Solder	Solder

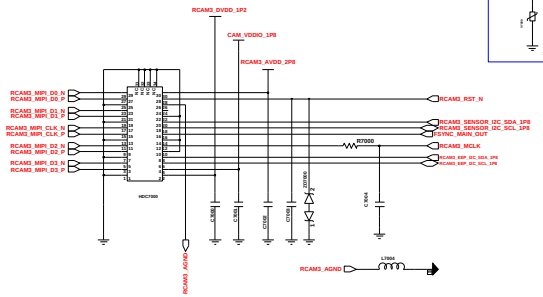
Engineer:	
Model No:	AUDIO, SIM/SD, SHIELD CAN
PBA CHK:	
STYLE:	2019 A505
EDC CTRL CHK:	
MFG ENGR CHK:	
QA CHK:	
REV:	
Drawing Number:	
Page: 67	

Changed by:	Date Changed:	Time Changed:

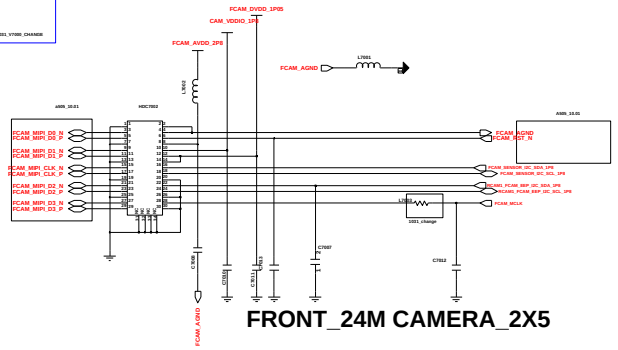
CAMERA_PART



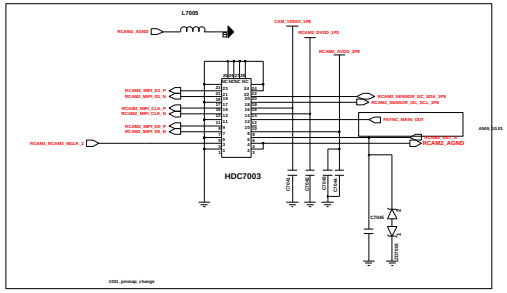
RCAM1_24M CAMERA(34PIN)_IMX576



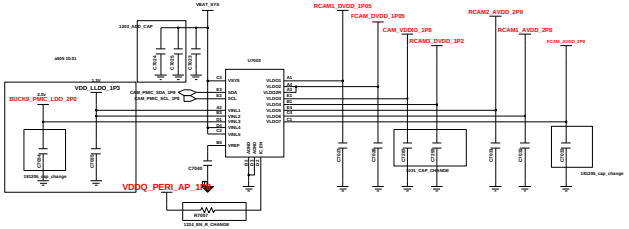
RCAM3_SUPER WIDE_8M CAMERA(30pin)_S5K4HA



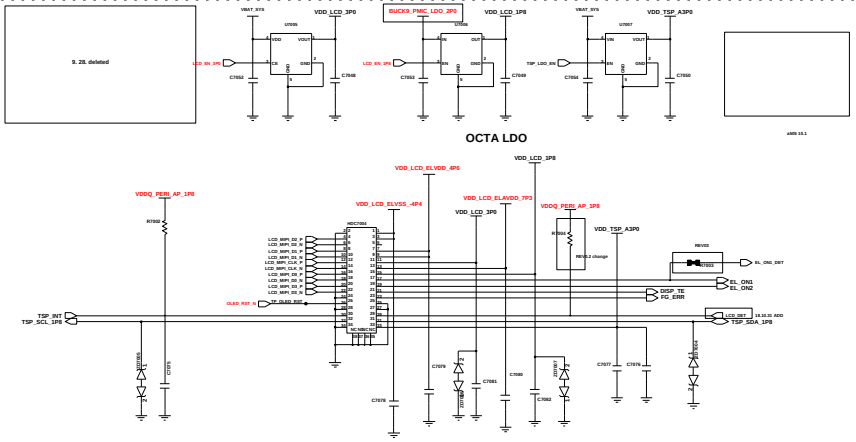
FRONT_24M CAMERA_2X5



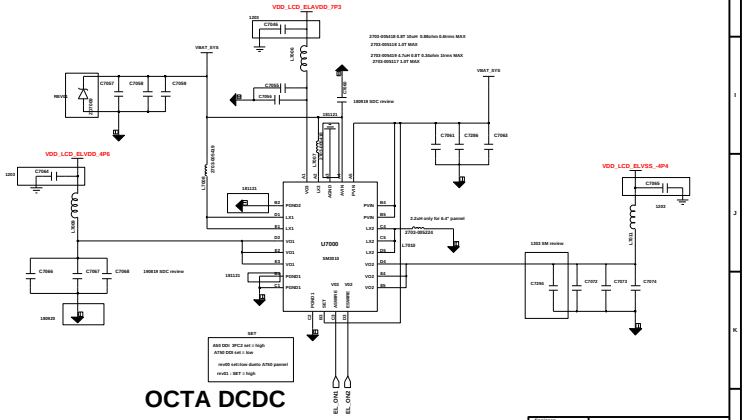
RCAM2_5M CAMERA(24PIN)_GC5035



CAM PMIC



OCTA LDO



OCTA DCDC

6.38" FHD+ DISPLAY

Engineer:	CAMERA, DISPLAY
Issued by:	
R&D CHK:	
DOC CTRL CHK:	TITLE: 2010 A505
MFG ENGR CHK:	
QA CHK:	

Changed by:	Date Changed:	Time Changed:	REV:	Drawing Number:	Page: 7/7